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PATENT
Attorney Docket No.: 019717-002700US
Client Reference No.: BP1884

On July 9, 2002

TOWNSEND and TOWNSEND and CREW LLP

By: DJ Bogorodsky
DJ Bogorodsky

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jun Cao

Application No.: 09/955,693

Filed: September 18, 2001

For: LINEAR PHASE DETECTOR FOR
HIGH-SPEED CLOCK AND DATA
RECOVERY

Examiner: Stephen Chin

Art Unit: 2634

INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

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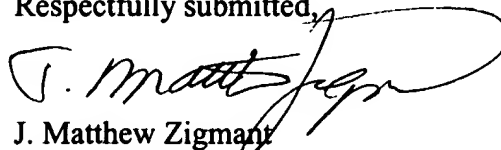
The references cited on attached form PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

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Respectfully submitted,



J. Matthew Zigmont
Reg. No. 44,005

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Application Number 09/955,693

Filing Date September 18, 2001

First Named Inventor Cao, Jun

Group Art Unit 2634

Examiner Name Stephen Chin

Attorney Docket Number 019717-00270008

Total # of Pages in This Submission

5 w/9 refs.

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Document(s)☐ Response to Missing Parts/
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Parts under 37 CFR
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Townsend and Townsend and Crew LLP

J. Matthew Zigmant

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 1

Complete If Known

Application Number 09/955,693

Filing Date September 18, 2001

First Named Inventor Cao, Jun

Art Unit 2634

Examiner Name Stephen Chin

Attorney Docket Number 019717-002700US JUL 22 2002

Technology Center 2600

U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US-4,535,459	08-13-1985	Hogge, Jr.	
	AB	US-5,301,196	04-05-1994	Ewen et al.	
	AC	US-09/784,419	filed 02-15-2001	Jun Cao	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AF	C.R. HOGGE "A self-correcting Clock Recovery Circuit", IEEE Journal of Lightwave Technology, vol. LT-3, pp. 1312-1314, Dec. 1985	
	AG	E. MULLNER, "A 20 Gb/s Parallel Phase Detector and Demultiplexer Circuit in a Production Silicon Bipolar Technology with $f_T=25$ GHz", Proc. IEEE BCTM, pp. 43-45, Oct. 1996	
	AH	M. RAU et al., "Clock/Data Recovery PLL Using Half-Frequency Clock", IEEE Journal of Solid-State Circuits, vol. 32, no. 7, July 1997	
	AI	K. NAKAMURA et al., "A 6 Gbps CMOS Phase Detecting DEMUX Module Using Half-Frequency Clock", IEEE, 1998 Symposium on VLSI Circuits Digest of Technical Papers	
	AJ	M. WURZER et al., "40 Gb/s Integrated Clock and Data Recovery Circuit in a Silicon Bipolar Technology", IEEE, 1998	
	AK	JAFAR SAVOJ et al., "A 10-Gb/s CMOS Clock and Data Recovery Circuit", IEEE, 2000 Symposium on VLSI Circuits Digest of Technical Papers	

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